

The diagram illustrates a multi-channel video signal processing system. It features a series of input channels, each consisting of a preamplifier (41₁, 41₂, 41₃, ..., 41_{n-1}, 41_n) and a corresponding processing block (43₁, 43₂, 43₃, ..., 43_{n-1}, 43_n). The preamplifiers are connected to the processing blocks via a common bus. Each processing block has inputs for ENABLE, LEFT, VETO, RIGHT, and CENTER, and outputs for PULSE_IN and PULSE_OUT. The PULSE_OUT signals from all processing blocks are connected to a central OR gate (71), which produces the final output signal, TOTAL_OUT. The system is controlled by an ENABLE_VIDEO signal (61) and a GND signal.

FIG-2

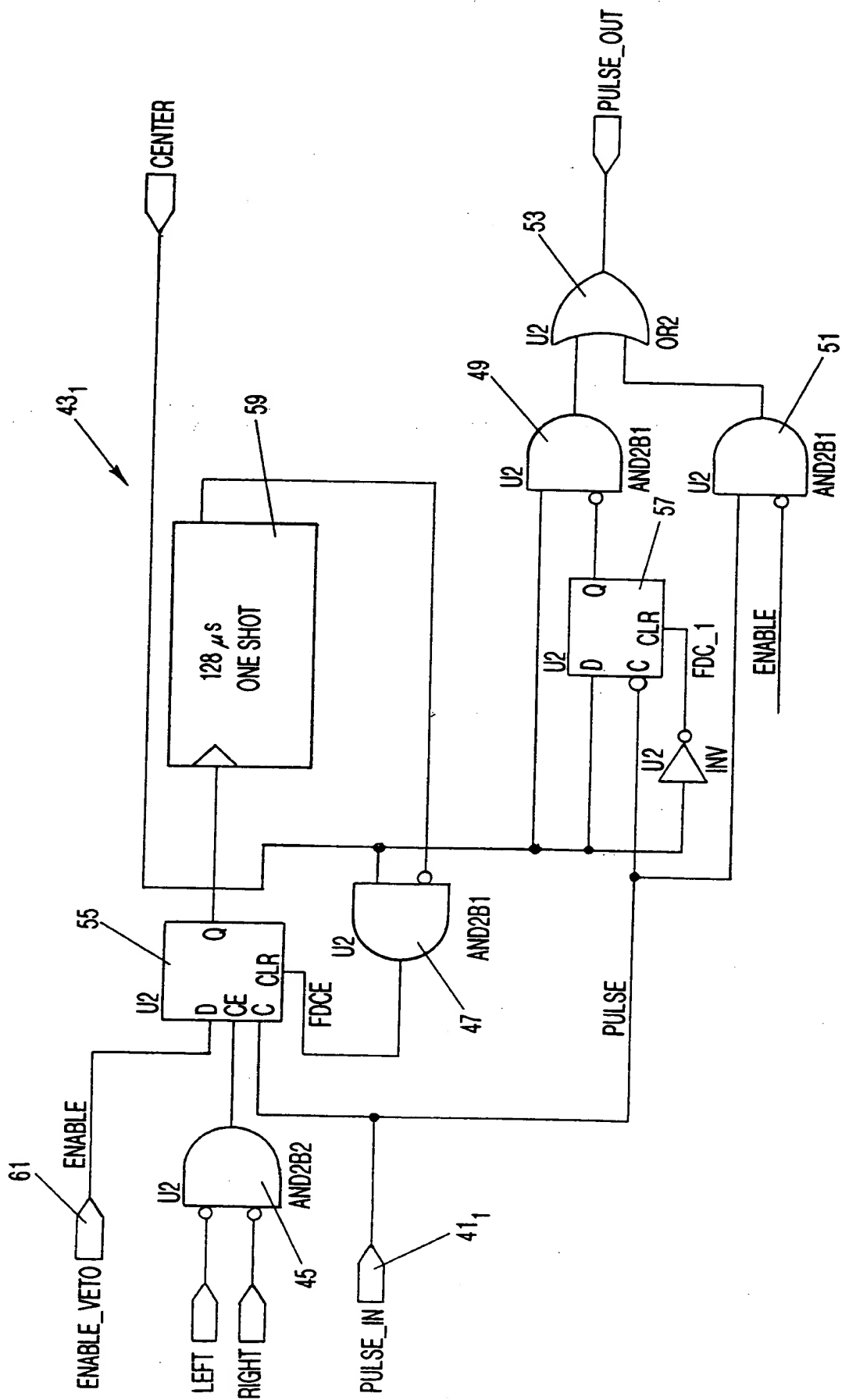


FIG-3